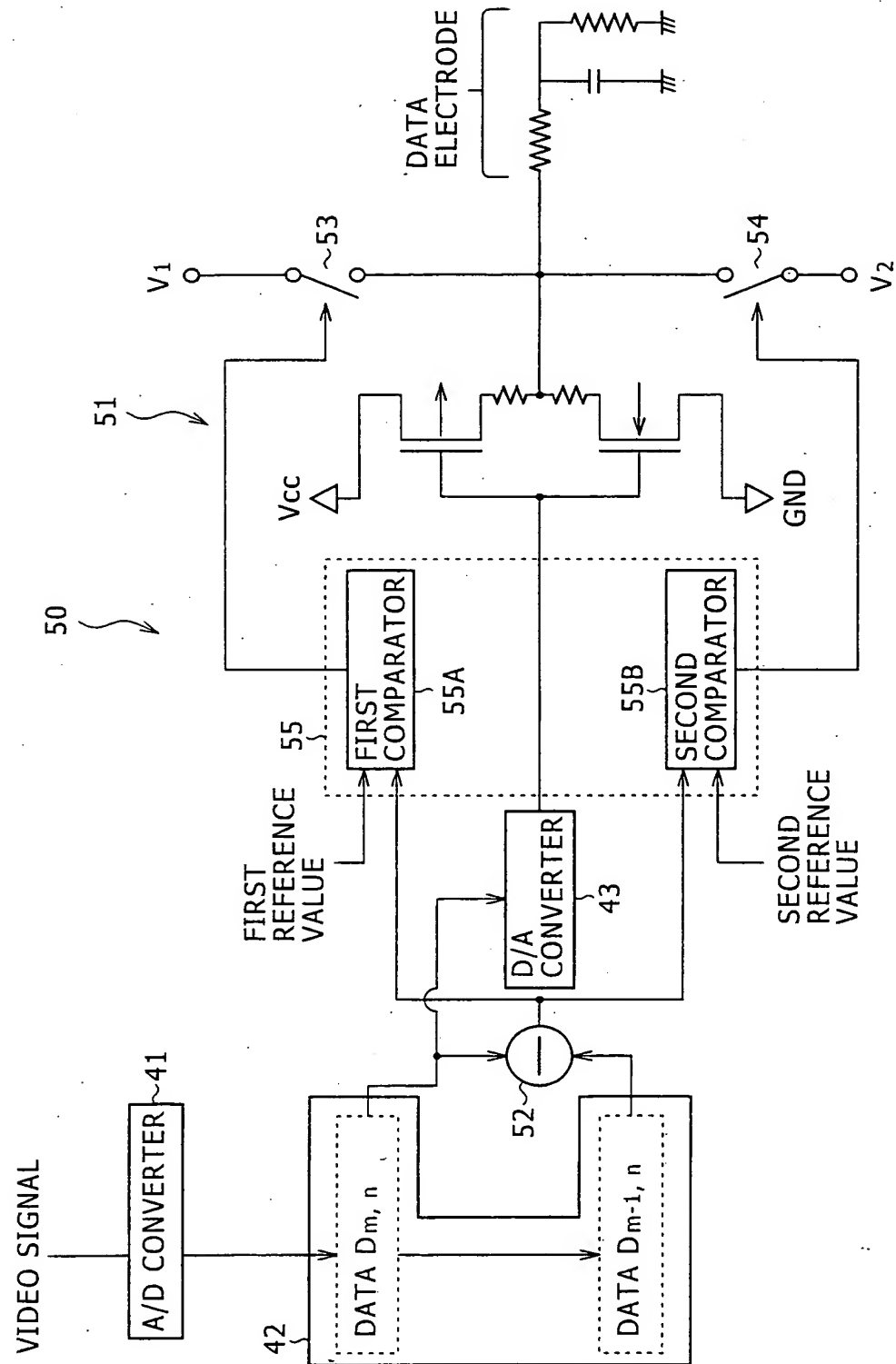
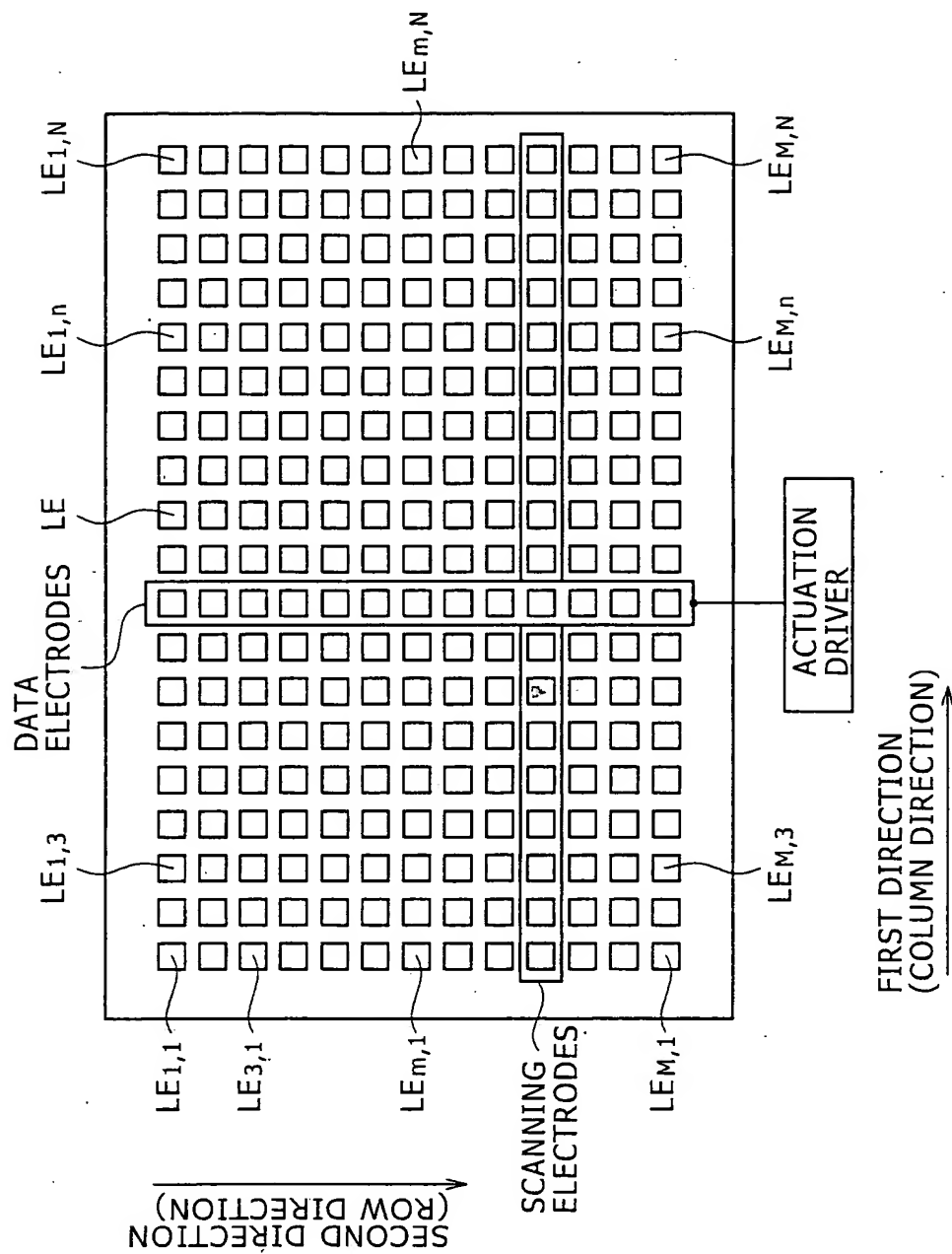


FIG. 1



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FIG. 2



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FIG. 3A

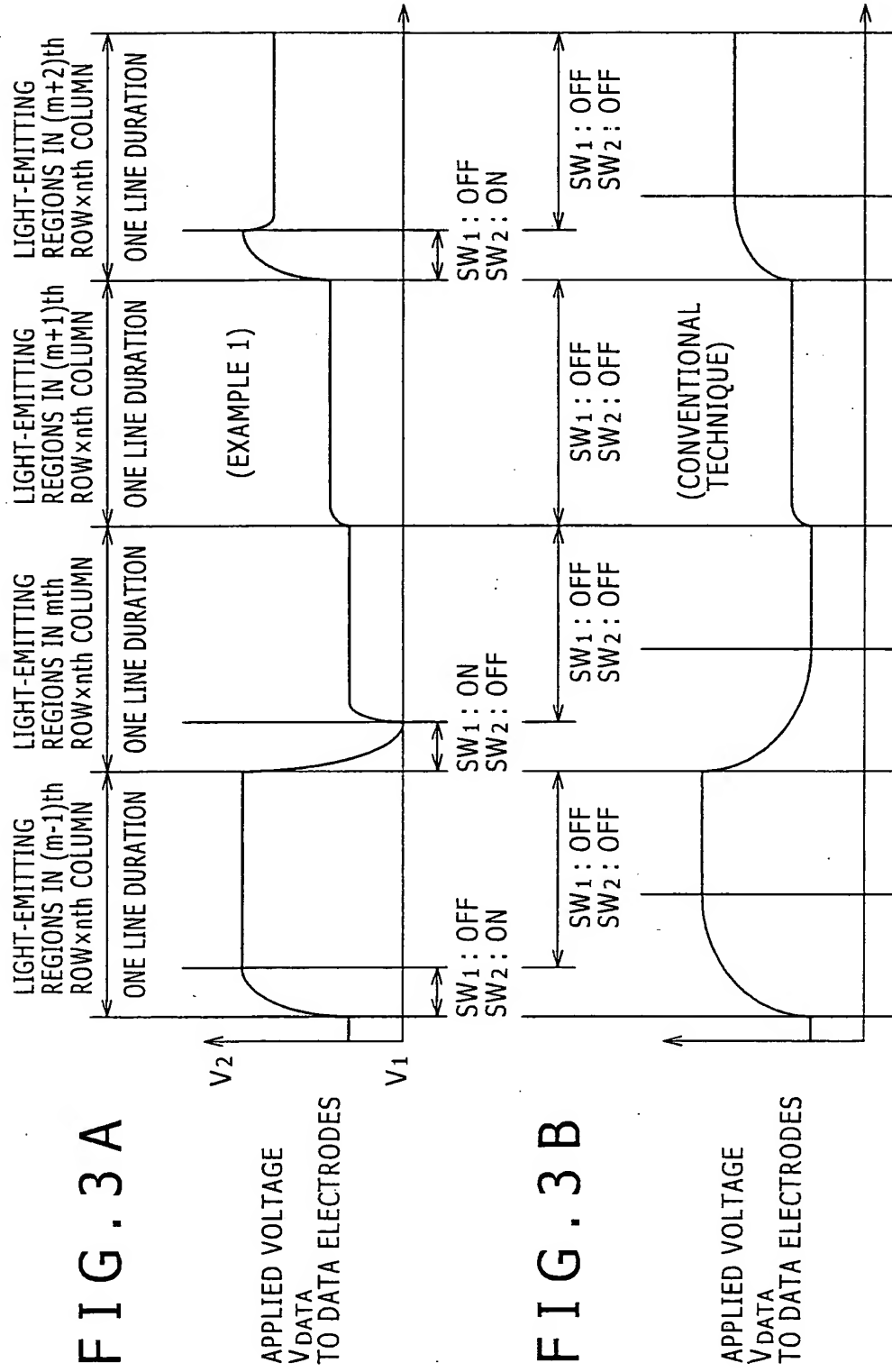


FIG. 3B

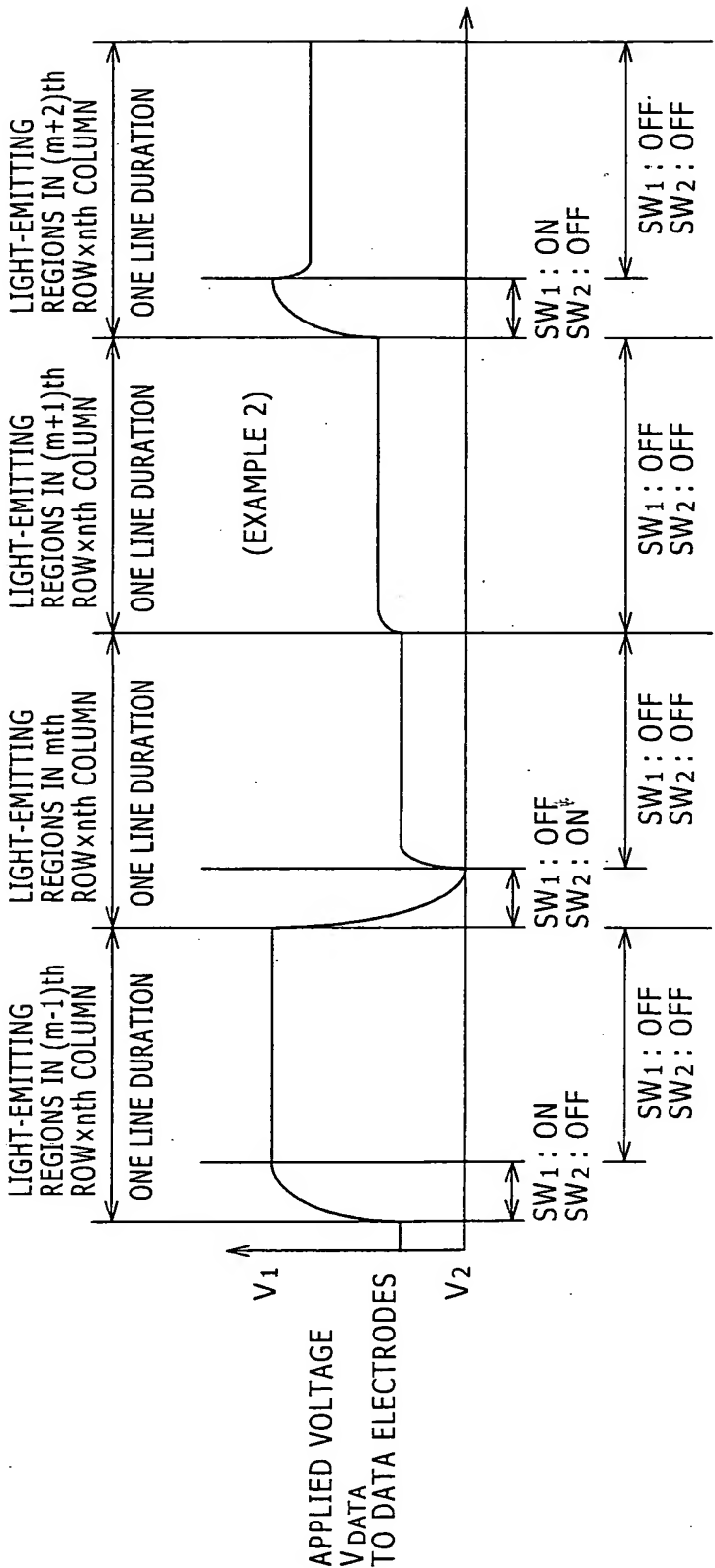
APPLIED VOLTAGE V_{DATA} TO DATA ELECTRODES

FIG. 3B illustrates the timing of data voltage application to data electrodes for four light-emitting regions in the m th row and x th column, using a conventional technique. The regions are labeled: LIGHT-EMITTING REGIONS IN $(m-1)$ th ROW x th COLUMN, LIGHT-EMITTING REGIONS IN m th ROW x th COLUMN, LIGHT-EMITTING REGIONS IN $(m+1)$ th ROW x th COLUMN, and LIGHT-EMITTING REGIONS IN $(m+2)$ th ROW x th COLUMN. Each region is associated with a "ONE LINE DURATION" period. The timing is defined by the state of two switches, SW_1 and SW_2 , and the applied voltage V_{DATA} .

For the first region (leftmost), SW_1 is OFF and SW_2 is OFF during the first half of the line duration. For the second region, SW_1 is OFF and SW_2 is OFF during the first half. For the third region, SW_1 is OFF and SW_2 is OFF during the first half. For the fourth region, SW_1 is OFF and SW_2 is OFF during the first half. The voltage V_{DATA} is applied to the data electrodes during the first half of each line duration. The voltage V_1 is applied to the data electrodes during the second half of each line duration. The voltage V_2 is applied to the data electrodes during the first half of each line duration.

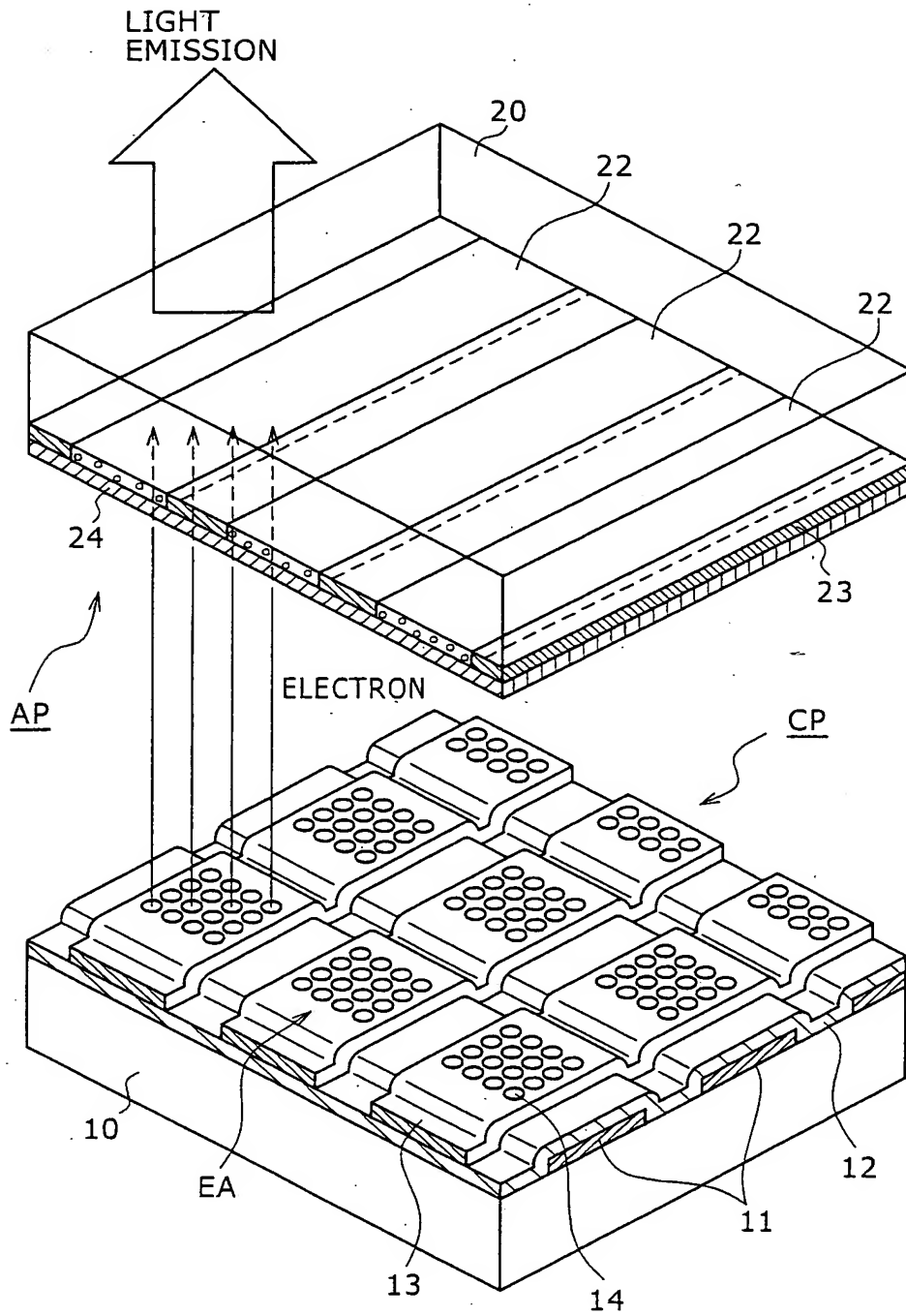
(CONVENTIONAL TECHNIQUE)

FIG. 4



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FIG. 6



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FIG. 7

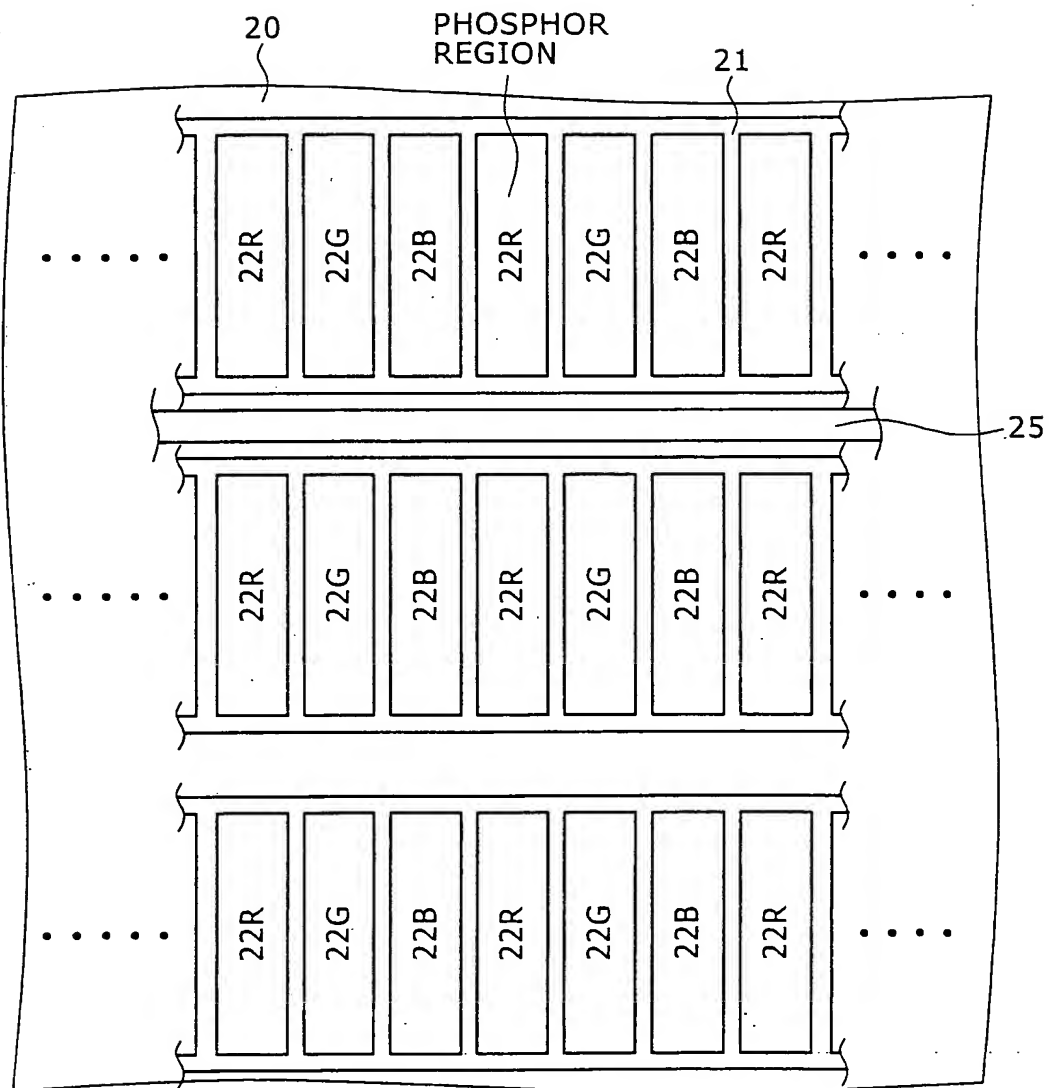
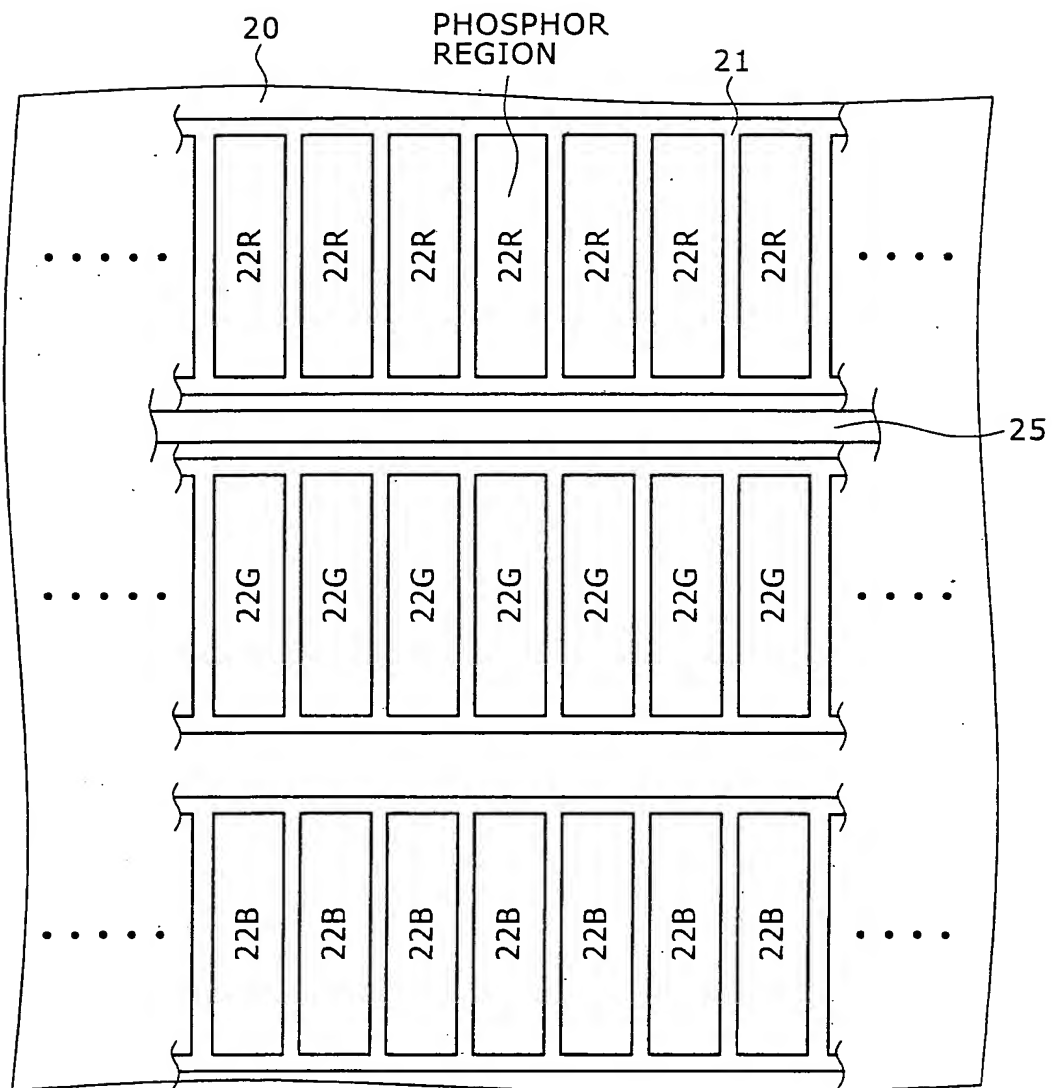
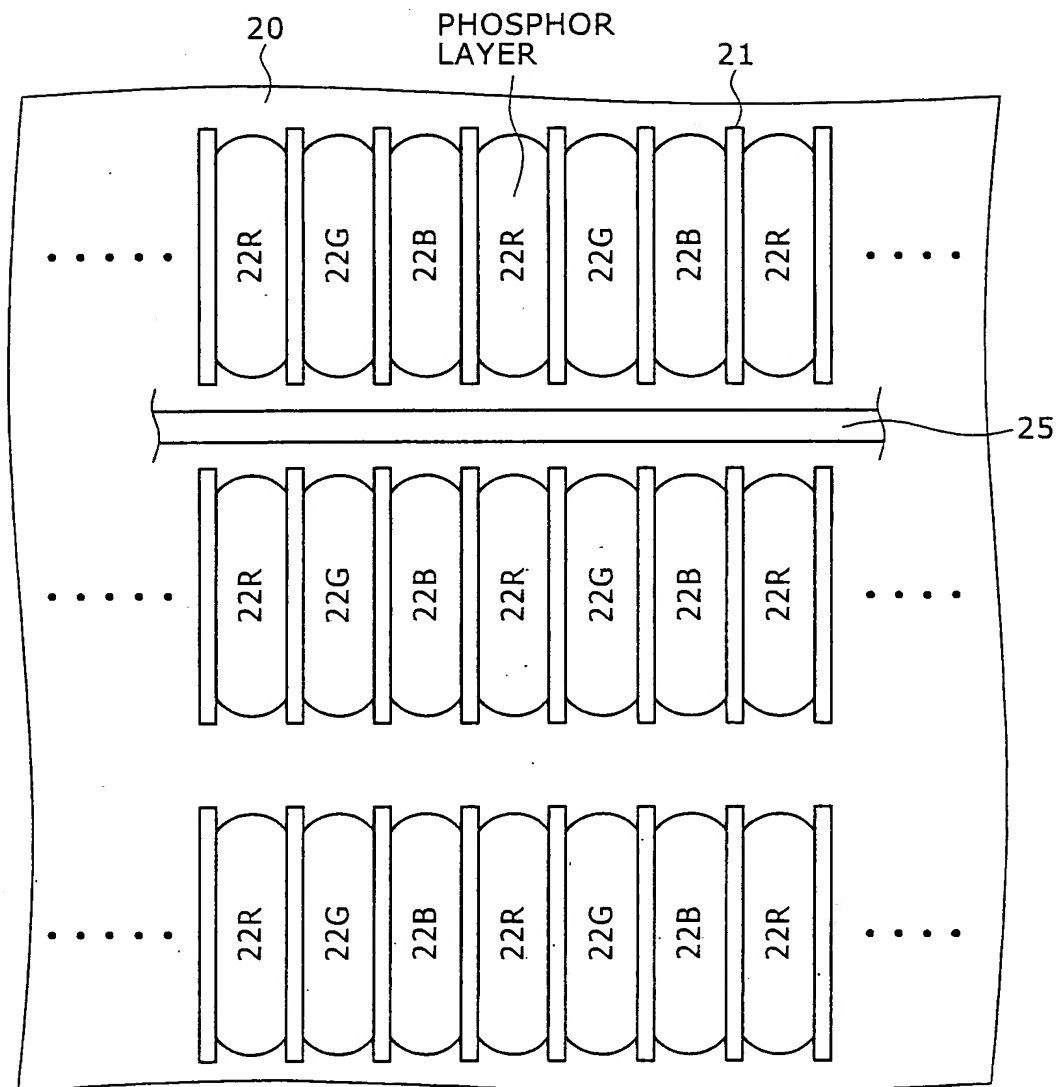


FIG. 8



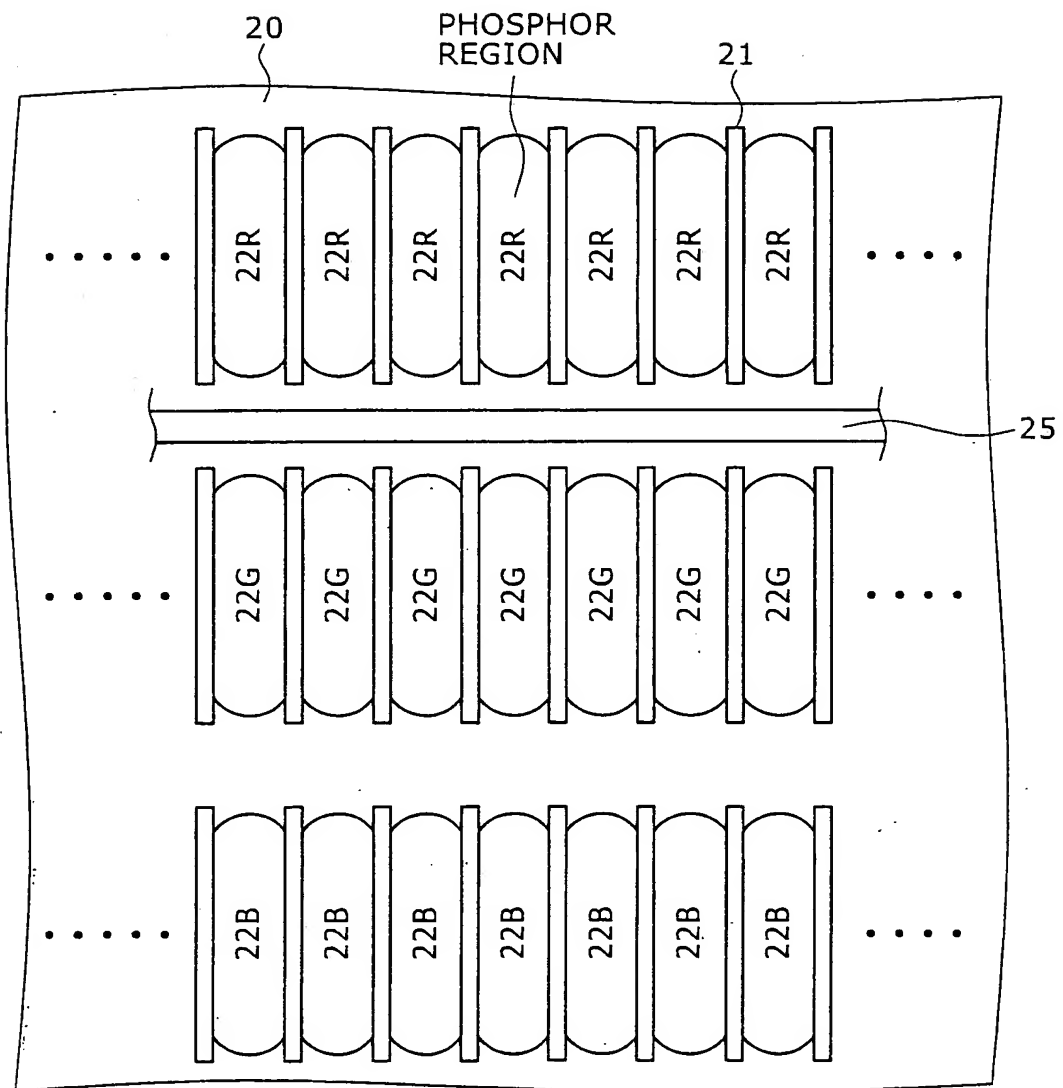
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FIG. 9



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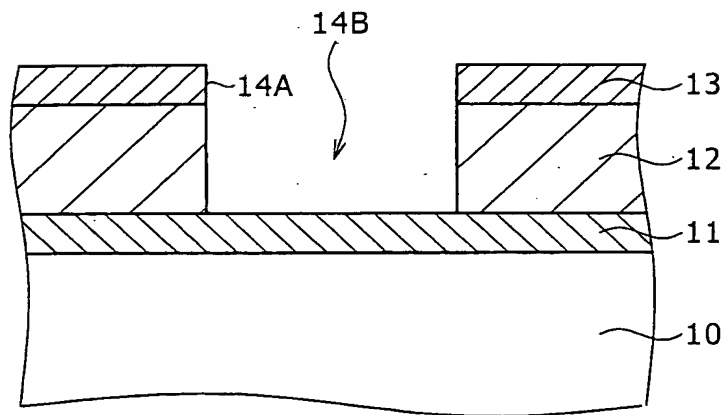
FIG. 10



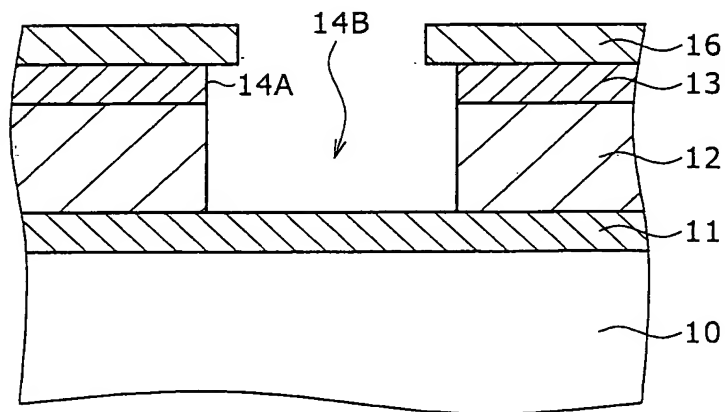
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FIG. 11 A

[STEP — A2]

**FIG. 11 B**

[STEP — A3]



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FIG. 12A

[STEP — A4]

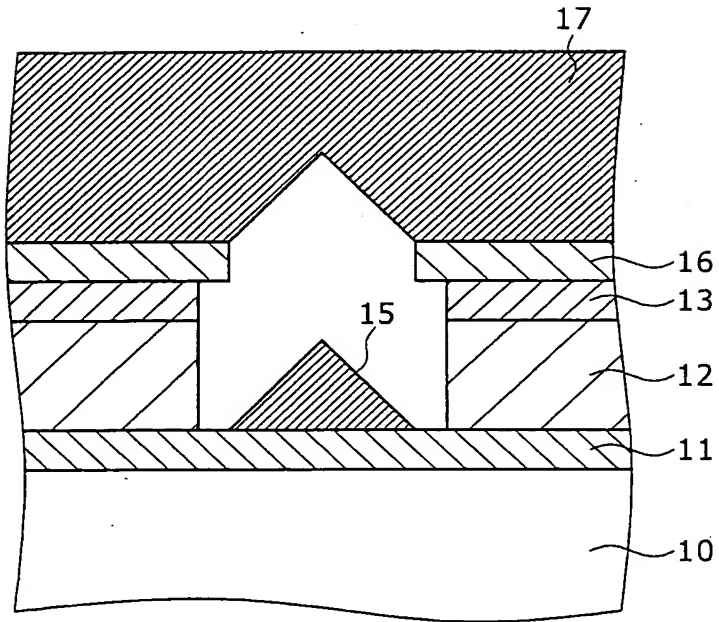
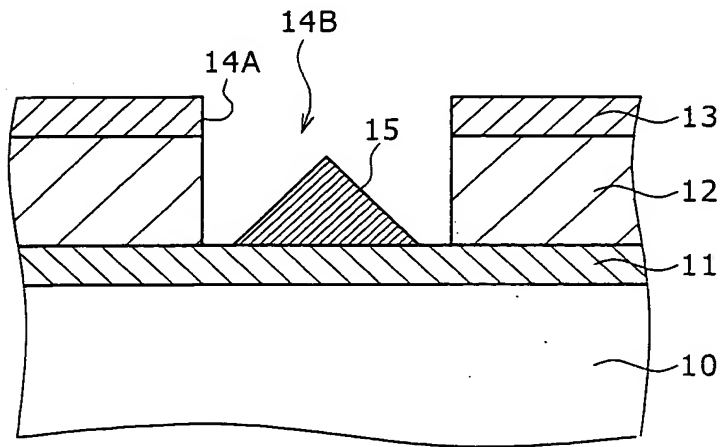


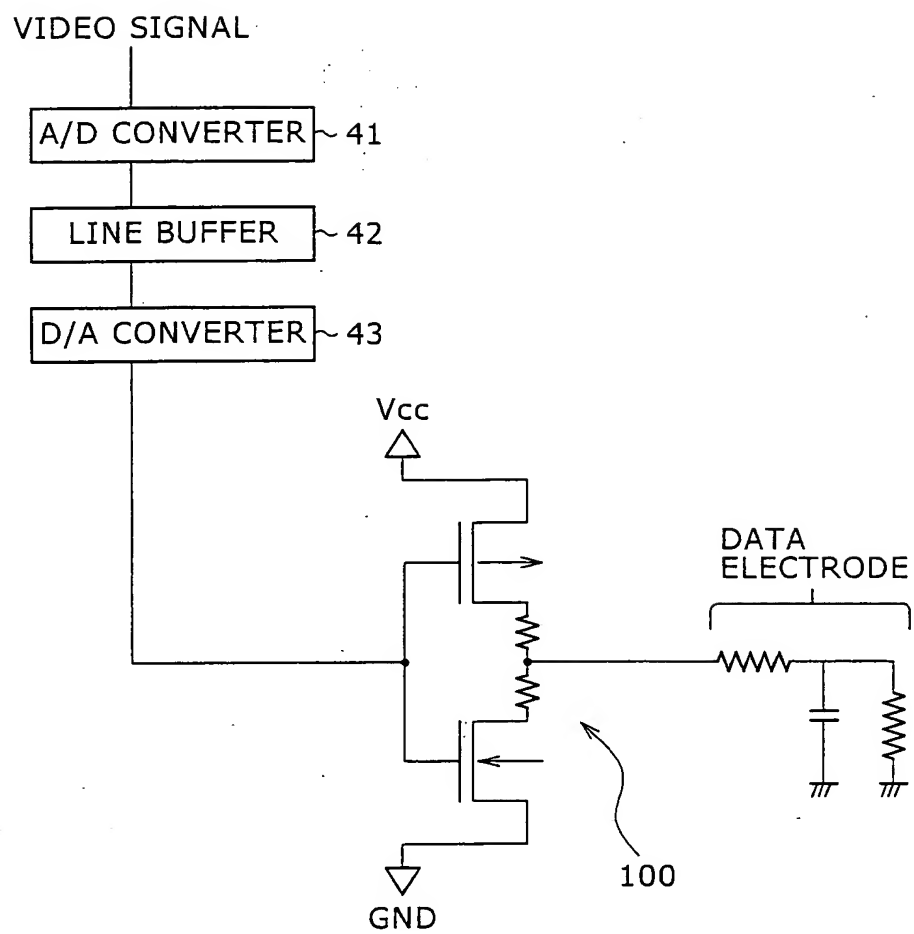
FIG. 12B

[STEP — A5]



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FIG. 14



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